





AC-coupled n-in-p pixel detectors on MCz silicon with atomic layer deposition (ALD) grown thin film process

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Outline



- 1. HIP CMS upgrade and research projects
 - HIP location and background
 - Project members
- 2. Detector R&D
 - For prototype processing
 - Detector process flow



Dr. Jaakko H. (Photograph 2004)

3. Characterization and selected results

Summary

HIP location and background





- The institute is responsible for the Finnish research collaboration with CERN.
- The HIP CMS Upgrade Project is in charge of the Finnish participation in the CMS Tracker and its future upgrade, as well as participation in the (silicon) endcap region of the CMS MIP Timing Detector.



<u>CMS experiment upgrade</u> and detector R&D



Compact Muon Solenoid (CMS) experiment at CERN LHC

- Finland had a significant role in the Phase I pixel upgrade: delivery of 250 full pixel modules +
- Currently the modules in the innermost layer of the CMS pixel detector need to be replaced
 - UBM, Plating and Flip chip bonding: Advacam
 - BB Quality assurance: HIP



HIP CMS upgrade and **RBI** PaRaDeSEC project teams





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CMS experiment upgrade and <u>detector R&D</u>



- Next generation pixel sensor (general requirements)
 - Radiation hardness
 - Relatively low costs
 - Feasibility in large scale / industrial production
 - Robustness

Measurement and characterization

→ HIP knowledge and skills



FOR PROTOTYPE PROCESSING

Micronova and Nanofabrication Center cleanroom facilities in Espoo



Main Cleanroom spec.

Total Area2 600 m²(M1, M2 with AaltoUniv.)Process toolIC, non ICWafer size4 - 8 inchCleanroom Classification

ISO 4...ISO 6 (10...1000)

Clean baysTemperature21 °C ± 0,5 °CRelative humidity45 % ± 5%

Magnetic Czochralski silicon (MCz-Si)



- High resistivity MCz-Si
 - High level of oxygen (typically 10¹⁶-10¹⁷ cm⁻³)
 - Adjustable oxygen content by magnetic fields
 - Available over 150 mm wafer sizes
 - Depletion layer affected by

 a) oxygen concentration
 b) thermal history (process)
 - Cost effectiveness compared to FZ

Atomic Layer Deposition (ALD)



- ALD is pinhole free deposition
- ALD results in conformal conformal coating
- ALD provides interesting material thin films
 - Metal oxides e.g. Al2O3, TiO2, SnO2, ZnO, HfO2 etc.
- ALD can tailor amount and type of oxide charge





Conformal coverage in ALD [1]S.Franssila



Beneq TFS-500 batch-type ALD reactor







DETECTOR PROCESS FLOW



Process Flow



(HSTD12) at Hiroshima, Japan

Detector design and outcome





Pixel array: 52 columns × 80 rows = 4160 pixels

Detector bias design



PSI46dig pixel detector



Bias connection via AI metal line

Bias connection via Implantation window

[2] J. Ott et al., [3] Jaakko Härkönen

Solder bumps



- The read out chip (ROC) is PSI46dig used in CMS experiment.
- Solder bump (~29um dia.)





Hybridization



- Flip Chip bonder
 - Align X,Y and Θ (microscope optics)
 - Leveling (autocollimator)
 - Thermocompression bonding (°C, Kg ,Sec.)
 - > Alignment accuracy: 0.5um
 - Post_biond accuracy: 3um









CHARACTERIZATION AND SELECTED RESULTS



12th International "Hiroshima" Symposium on the Development and Application of Semiconductor Tracking Detectors (HSTD12) at Hiroshima, Japan



TCT area scans for 50 μ m pixels









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Source test HIP AC-coupled pixel detector





Summary



- We fabricated 150mm p-MCz Si wafers with our sensor design.
- Detectors are processed at Micronova / Nanofabrication center in Finland.
- Atomic Layer Deposition (ALD) technology has many properties for radiation detector fabrication processes.
- AC-coupling of small pixels connected with each other by metal-nitride thin film bias resistors.
- Flip-Chip bonding of sensor and CMS PSI 46dig CMOS ROC was performed.
- Pixel detectors were tested both HIP and RBI.
- Currently, protective passivation by ALD-HfOx is being researched (ref: see slide 11 right picture).







Dr. Jaakko H. (Photograph)

* Citations



- 1. Sami Franssila, John Wiley & Sons, Incorporated 2010, Introduction to Microfabrication Second Edition
- 2. J. Ott et al., Presented at 33rd RD50 Workshop, CERN, Geneva, Switzerland, November 28th 2018, Processing of pixel detectors on p-type MCz silicon using atomic layer deposition (ALD) grown aluminium oxide <u>https://indico.cern.ch/event/754063/contributions/3222806/attachments/17607</u> <u>72/2865963/JOtt_RD50_Nov18_3.pdf</u>
- 3. Jaakko Härkönen, Presented at Croatian Particle Physics Days, Dec 09 2019
- 4. Esa Tuovinen, PROCESSING OF RADIATION HARD PARTICLE DETECTORS ON CZOCHRALSKI SILICON (Doctoral dissertation), 2008

http://paradesec.irb.hr/ http://research.hip.fi/hwp/cmsupg/ http://research.hip.fi/detlab/index.htm http://www.micronova.fi/



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Backup slides: from Dr. Jaakko H.

"When pixel technology meets Arts...." by Jaako Härkönen





Backup slides: ALD process for metal oxide deposition





(A)
$$AI-OH^* + AI(CH_3)_3$$
 $AI-O-AI-(CH_3)_2^* + CH_4$
(B) $AI-CH_3^* + H2O$ $AI-OH^* + CH_4$

 $2\mathsf{AI}(\mathsf{CH}_3)_3 + 3\mathsf{H}_2\mathsf{O} \qquad \mathsf{AI}_2\mathsf{O}_3 + 6\mathsf{CH}_4$



Ref. https://www.glassonweb.com/article/atomic-layer-deposition-glass-industry